

Announcement

NASECODE IV Conference

Trinity College, University of Dublin, Ireland, June 1985

The NASECODE IV Conference will comprise the Fourth International Conference on the Numerical Analysis of Semiconductor Devices and Integrated Circuits, on 19–21 June 1985, and two related events, a short course on New Problems and New Solutions for Device and Process Modeling, on 17–18 June 1985, and a workshop on An Introduction to Circuit-Level Simulation for VLSI Design and Verification, on 24–25 June 1985.

Abstracts of 20-minute contributed papers on topics relevant to the Conference should be submitted to the Organizing Committee by 15 February 1985. Notification of acceptance will be mailed by 15 March. Abstracts should be at most one A4-page in length.

For further information and to submit abstracts, please contact:

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